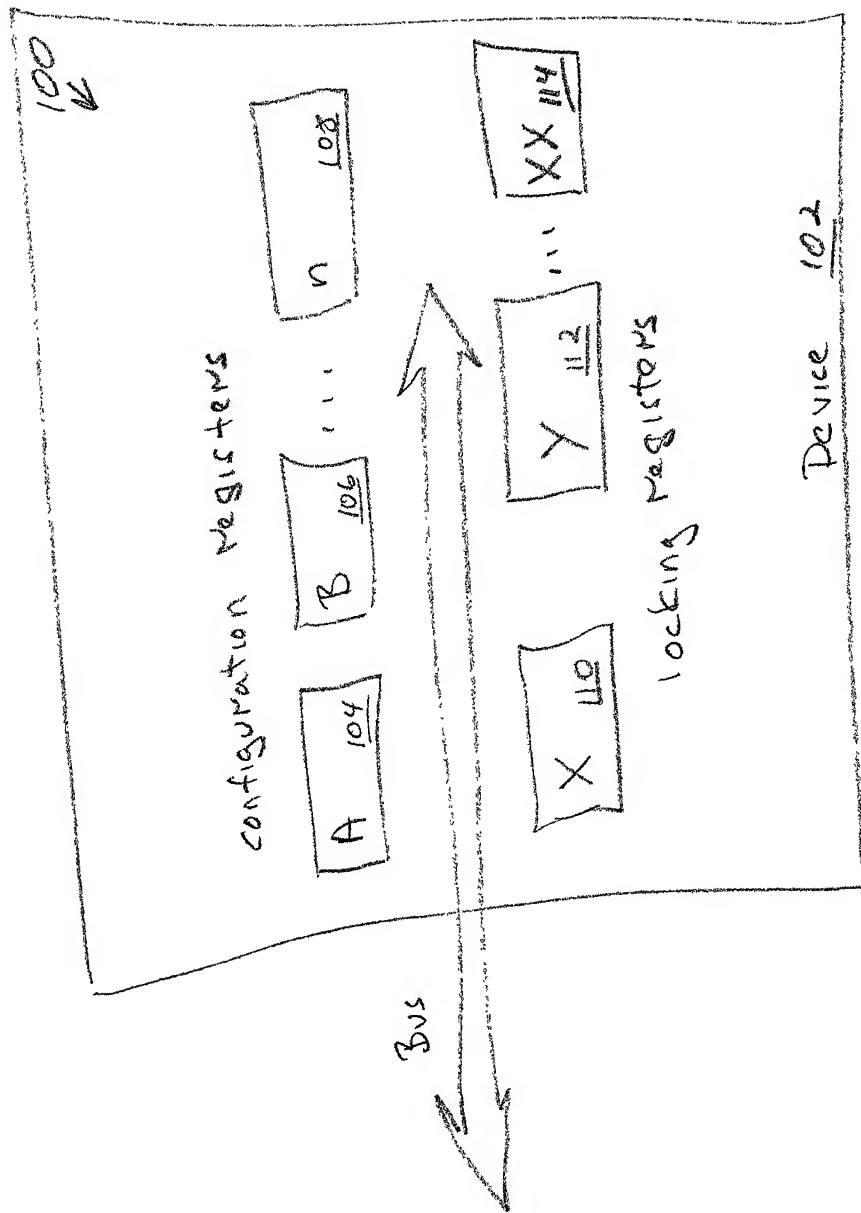


[illegible]

configuration

104

2

96

10

Bus

X

3

10

X

locking registers

Device 102

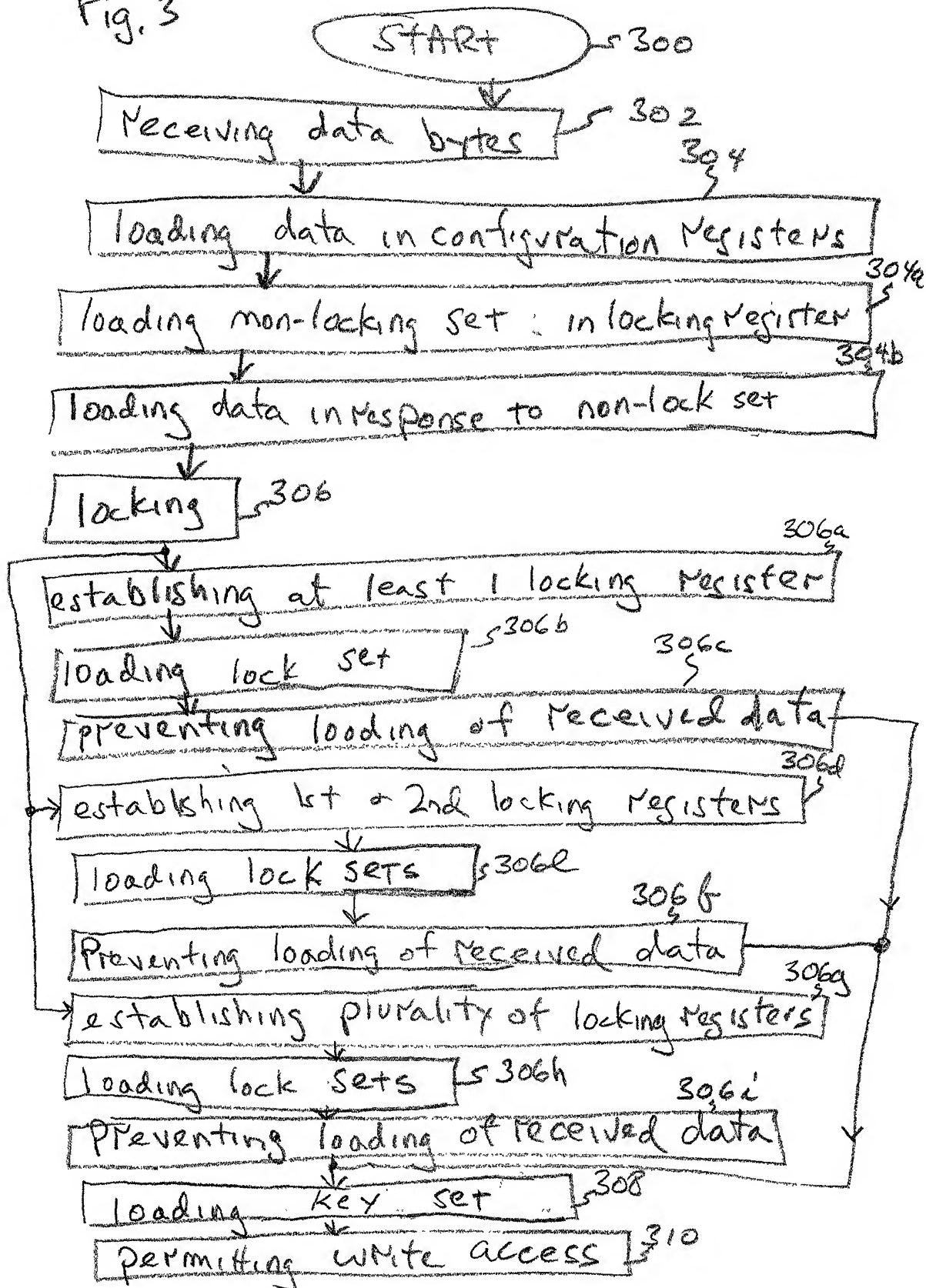
5

Address → 1111000 1111000 1111000000

X "01010101"		
		Y "01010101"
	A	
	B	
	n	XX "01010101"

Fig. 2

Fig. 3



10023016-121804

10033019-131501
TEST REPORT

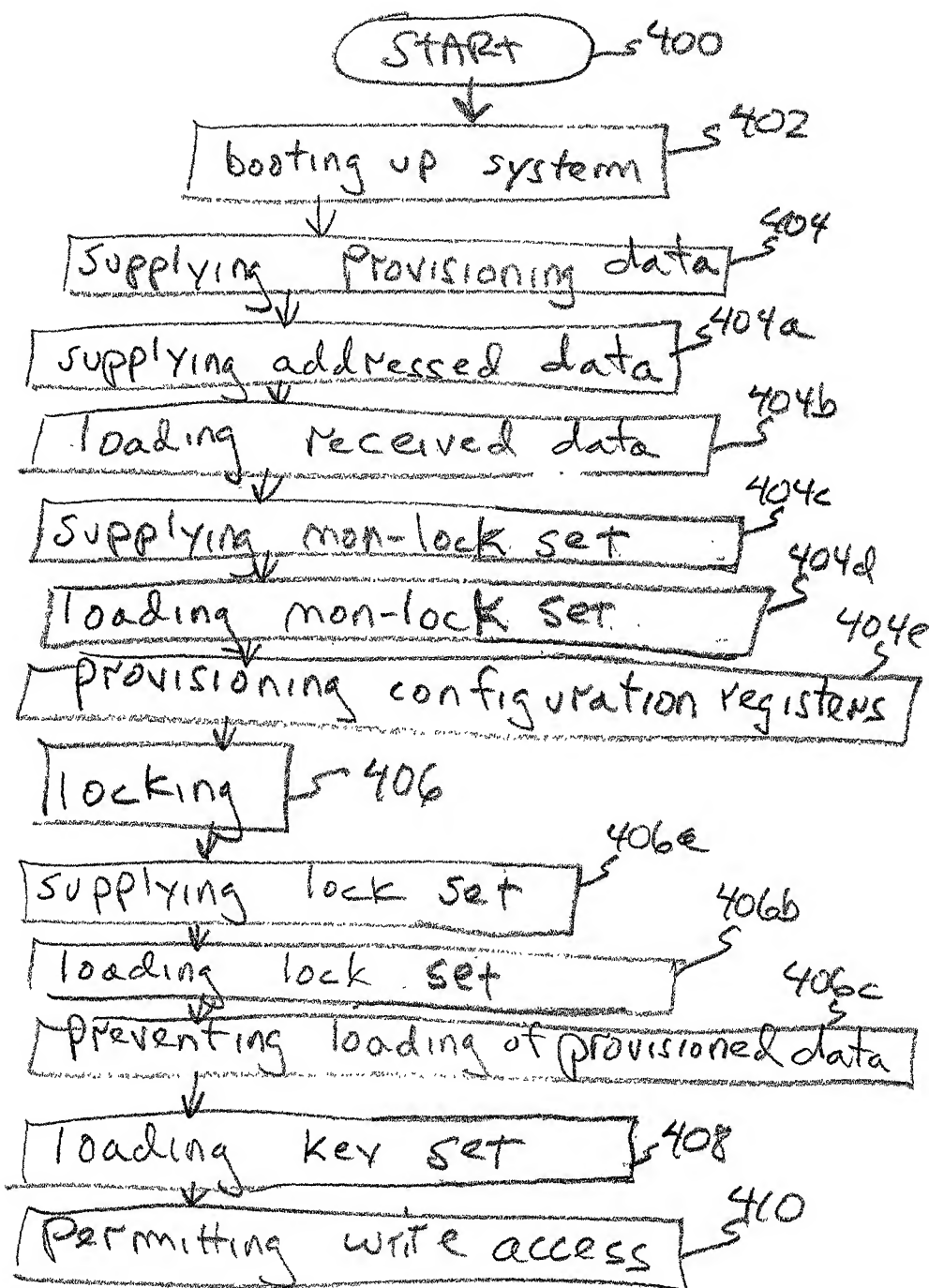


Fig. 4